



IMPORTANT DATES

Receipt of full length paper : 30/01/2010

Intimation of acceptance : 06/02/2010

Receipt of camera-ready paper : 13/02/2010

Last date of registration : 13/02/2010

REGISTRATION FEE

Industry Participants : Rs. 1000/-

Academic Institute Participants : Rs. 500/-

Research Scholar and Students : Rs. 250/-

Registration fee should be paid as demand draft in favour of **Gyan Ganga Institute of Technology & Sciences**, Payable at Jabalpur. The enclosed registration form along with registration fee should be sent to Convenor on or before the due date. The registration fee includes conference materials, lunch, dinner and refreshment.

Accommodation

On prior request accommodation for the participants can be arranged on payment basis at nearby hotels in Jabalpur. The tariff ranges from Rs. 500/- to Rs. 3000/- depending upon the type of accommodation.

ADDRESS FOR CORRESPONDENCE

Mr. Vinod Kapse

Convenor, ATVES-2010

Department of Electronics & Communication Engineering

Gyan Ganga Institute of Technology & Sciences, Jabalpur

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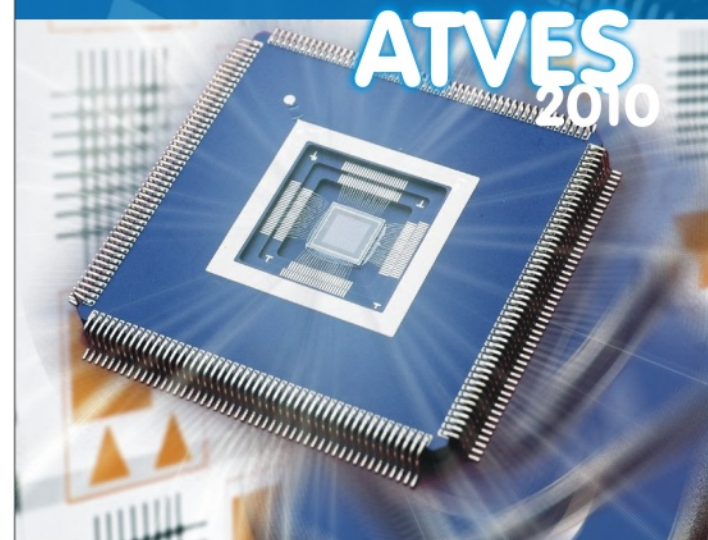
• Prof. Soma Pandey, CEO, Accelerated Systems Pvt. Ltd.

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National Conference on Advancements & Future Trends in VLSI Design & Embedded System

23 - 24 February, 2010

ATVES 2010



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THEME

The objectives of the "National Conference on Advancements & Future Trends in VLSI Design & Embedded System" is to create awareness about modern developments in the area of VLSI Technology and Embedded System and promote R&D activities for real life applications. This Conference on VLSI Design is a forum that brings together researchers and designers to discuss various aspects of VLSI design, electronic design automation (EDA), enabling technologies in embedded systems. This conference will bring an opportunity for industry professionals, researchers and academicians to present their findings and have intellectual deliberations on emerging technologies.

This two-day conference features panel discussions, regular paper sessions, special sessions and design contest. The aim of the conference is to cover the two vital areas of semiconductor industry, VLSI and embedded systems.

ABOUT THE INSTITUTION

Gyan Ganga Institute of Technology & Sciences, Jabalpur is an ISO 9001:2008 Certified & NBA accredited institute established in the year 2003. It is affiliated to Rajiv Gandhi Pradyogiki Vishwavidyalaya, Bhopal & is an AICTE approved institution. The Gyan Ganga family has reiterated its commitment to impart technical education to a broader cross section of population in the country in general and in Mahakaushal & Madhya Pradesh in particular by opening two more Engineering colleges namely Gyan Ganga College of Technology in Jabalpur(2006) & Gyan Ganga Institute of Technology & Management in Bhopal (2007). The institute of the group provides excellent infrastructure, well equipped laboratories, large computer centers with high bandwidth and WiFi internet facility and library with large number of books on different subjects and National/International Journals internet access. Modern facilities and amenities are available in the campus. A highly proactive Training & Placement cell is fully functional. GGITS offers B.E. Degree courses in Electronics & Communication Engg., Computer Science & Engg., Mechanical Engg., Electrical Engg., Information Technology, Civil Engg., Electrical & Electronics Engg., and Post Graduate Courses in M.Tech. in Embedded System & VLSI Design & Energy, M.B.A. & M.C.A.

LOCATION

The Institute is functioning in an area of about 25 acres at Tilwara Ghat Road near Bargi Hills in Jabalpur district of Madhya Pradesh with lush greenery around the campus & close to the banks of Holy river Narmada and is 10 Kms from main bus stand & Railway Station of Jabalpur on National Highway (NH-07). The city of Jabalpur is well connected with the rest of the country by rail or road. The nearest airport is at Jabalpur (Dumna) (20Kms). Direct air link from Delhi, Indore & Bhopal to Jabalpur is available. The place has healthy climate with moderate temperature ranging from 15°C to 30°C in the month of February.

ABOUT THE DEPARTMENT

The department offers B.E. in Electronics & Communication Engineering & M.Tech programme in Embedded System & VLSI Design. The department is accredited by NBA. The department has well equipped laboratories. Internet access to the Institute is provided through 4 Mbps (BSNL) link. Online access to International/National Journals and conference resources is available through Science Direct. The department has 30 well experienced, dedicated faculty members with different specializations.

CALL FOR PAPERS

Papers are invited from research scholars, engineers, faculty and students. The prospective authors are invited to submit their full length paper by email to atves10@ggits.org. The deadline for the submission of full paper is 30/01/2010. The papers must be submitted in electronic format (MS-Word or PDF). The papers must follow the IEEE format and should not exceed 6 pages. They will be independently peer-reviewed and acceptance will be notified.

TOPICS OF NATIONAL CONFERENCE

- VLSI Architecture Design Implementation & testing
- ASIC & System on Chip
- Analog/Digital Mixed signal IC Design & Circuits
- High Speed/Low-Power Circuit & Power Optimization Techniques
- VLSI Aspects of Sensor and Sensor Networks
- CAD Tools for Emerging Technology Devices and Circuits
- FPGA & CPLD Hardware Design
- VLSI Applications (Communications, Video, Security, Sensor Networks etc.)
- Digital Signal Processing
- Image Processing & Filtering
- Broadband Mobile Communication System
- 4G Wireless Communication
- Mobile Intelligent Networks
- Embedded Systems
- Artificial Intelligence
- Fuzzy Logic
- Pattern Recognition
- Biomedical Engineering
- Silicon on insulator

All the accepted papers would be published in the conference proceedings. At least one author has to present the paper in the conference.

National Conference on Advancements & Future Trends in VLSI Design & Embedded System

23 - 24 February, 2010

REGISTRATION FORM

Name:

Designation:

Organization:

Mailing Address:

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Delegate Type: Industry /Academics /Student

Accommodation Required: Yes/No

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